REMARKS

Claims 1-12 were pending in the application prior to entering this amendment.

Claims 1-12 were rejected.

Claims 1-12 were rejected under 35 U.S.C. 102(b).

The applicant adds no new matter and requests reconsideration.

Claim Rejections - 35 U.S.C. § 102

Claims 1-12 were rejected under 35 U.S.C. 102(b) as being anticipated by Soneda, et al., U.S. Patent No. 5,856,918 ("Soneda").

Applicant respectfully traverses the rejections.

Claims 1 and 6 have been amended to include a logic device having an output applied to the driving node and an input to receive the boosting level control signal. Support for this amendment can be found, for example, in FIG. 1 of the Specification. Here, a logic device O1 has an output applied to the driving node N1. No new matter has been added.

Soneda fails to show, teach, or anticipate the logic device included in the voltage boosting circuits recited in claims 1 and 6.

The Examiner considers node K1, shown in FIG. 2 of Soneda, to be the driving node recited in claims 1 and 6. This being the case, there is no logic device having an output applied to the driving node K1. Applicant recognizes the presence of logic circuitry at the CLK1 node shown in FIG. 2. No logic devices in this part of Soneda's circuit, however, have an output applied to the driving node K1. Thus, Soneda fails to anticipate each and every element of claims 1 and 6. Applicant therefore requests withdrawal of the rejections for these claims.

Additionally, claims 2-5 and 7-12 depend from claims 1 and 6, respectively, and inherently include all of the limitations of their base claims. As discussed above, the prior art does not teach the limitations of their base claim much less the further limitations of the dependent claims. Therefore, these claims are allowable for their dependency and their own merits. Allowance of these claims is requested.

New Claims

New claims 13-21 find support in, for example, FIG. 1 of the Specification. Each of these claims a logic device in a configuration that is not shown or suggested in the prior art.

Applicant requests that the Examiner initial and return a copy of Form PTO-1449 attached to the Information Disclosure Statement filed herein on February 28, 2005.

For the foregoing reasons, reconsideration and allowance of claims 1-21 of the application as amended is solicited. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Respectfully submitted,

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I hereby certify that this correspondence is being transmitted to the U.S. Patent and Trademark Office via facsimile number (571) 273-8300 on December 20, 2005.

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